

SMART CARD WITH SELF-RECONFIGURATION FEATURES AND RELATED METHODS

Abstract of the Disclosure

An integrated circuit for a smart card may include a transceiver and a processor for communicating with a host device via the transceiver. More particularly, the processor may provide at least one default descriptor to the host device, and cooperate with the host device to perform an enumeration based upon the at least one default descriptor. Moreover, the processor may also detect a system event and, responsive to the system event, provide at least one alternate descriptor to the host device and cooperate with the host device to perform a new enumeration based thereon.